

PALM INTRANET

Day : Sunday  
 Date: 3/4/2007  
 Time: 11:50:02

**Inventor Name Search Result**

Your Search was:

Last Name = TAI

First Name = YA-HSIANG

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09373250</a>	6323034	150	08/12/1999	AMORPHOUS TFT PROCESS	TAI, YA-HSIANG
<a href="#">09444141</a>	Not Issued	161	11/20/1999	LIQUID CRYSTAL DISPLAY HAVING STRIPE-SHAPED COMMON ELECTRODES FORMED ABOVE PLATE-SHAPED PIXEL ELECTRODES	TAI, YA-HSIANG
<a href="#">09826013</a>	6433485	150	04/05/2001	APPARATUS AND METHOD OF TESTING AN ORGANIC LIGHT EMITTING DIODE ARRAY	TAI, YA-HSIANG
<a href="#">09848257</a>	6432734	150	05/04/2001	METHOD OF MANUFACTURING A DISPLAY UNIT OF A FLAT DISPLAY PANEL HAVING A WIDE VIEWING ANGLE	TAI, YA-HSIANG
<a href="#">09848349</a>	Not Issued	161	05/04/2001	In-plane switching liquid crystal displaying device and method of fabricating the same	TAI, YA-HSIANG
<a href="#">09969997</a>	6801182	150	10/04/2001	SCAN DRIVING CIRCUIT AND DRIVING METHOD FOR ACTIVE MATRIX LIQUID CRYSTAL DISPLAY	TAI, YA-HSIANG
<a href="#">10434169</a>	7038276	150	05/09/2003	TFT WITH BODY CONTACTS	TAI, YA-HSIANG
<a href="#">10436795</a>	6965423	150	05/13/2003	LIQUID CRYSTAL DISPLAY AND FABRICATING METHOD THEREOF	TAI, YA-HSIANG
<a href="#">10460440</a>	7042437	150	06/12/2003	METHOD AND CIRCUIT FOR DRIVING LIQUID CRYSTAL DISPLAY	TAI, YA-HSIANG
<a href="#">10610056</a>	7023507	150	06/30/2003	COLOR FILTER HAVING A CONDUCTIVE SEGMENT ON AN INSULATING SEGMENT	TAI, YA-HSIANG

				THE CONDUCTIVE SEGMENT CONFIGURED TO ELECTRICALLY CONNECT AN ELECTRODE LAYER AT OPPOSITE SIDES OF THE INSULATING SEGMENT	
<u>10652055</u>	<u>6930372</u>	150	08/29/2003	STORAGE CAPACITOR OF PLANAR DISPLAY AND FABRICATION METHOD OF SAME	TAI, YA-HSIANG
<u>10691530</u>	<u>7123236</u>	150	10/24/2003	LEVEL SHIFTER WITH BODY-BIASED CIRCUIT	TAI, YA-HSIANG
<u>10714616</u>	Not Issued	30	11/18/2003	Method of charging liquid crystal display device	TAI, YA-HSIANG
<u>10904293</u>	Not Issued	161	11/03/2004	METHOD OF PATTERNING LOW-K FILM AND METHOD OF FABRICATING DUAL-DAMASCENE STRUCTURE	TAI, YA-HSIANG
<u>10904294</u>	Not Issued	161	11/03/2004	Non-volatile memory and method of manufacturing floating gate	TAI, YA-HSIANG
<u>10916006</u>	Not Issued	30	08/10/2004	Data driver and driving method thereof	TAI, YA-HSIANG
<u>11050576</u>	Not Issued	30	02/04/2005	Liquid crystal display and dummy loading device thereof	TAI, YA-HSIANG
<u>11162646</u>	Not Issued	161	09/18/2005	NON-VOLATILE MEMORY AND METHOD OF MANUFACTURING FLOATING GATE	TAI, YA-HSIANG
<u>11209896</u>	Not Issued	89	08/23/2005	Liquid crystal display and fabricating method thereof	TAI, YA-HSIANG
<u>11306813</u>	Not Issued	30	01/12/2006	ANALOG OUTPUT BUFFER CIRCUIT FOR FLAT PANEL DISPLAY	TAI, YA-HSIANG
<u>11356160</u>	Not Issued	30	02/16/2006	Source-follower type analogue buffer, compensating operation method thereof, and display therewith	TAI, YA-HSIANG
<u>11361634</u>	Not Issued	25	02/24/2006	Active matrix organic light emitting diodes pixel circuit	TAI, YA-HSIANG
<u>11394799</u>	Not Issued	30	03/30/2006	Method and circuit for driving liquid crystal display	TAI, YA-HSIANG
<u>11461780</u>	Not Issued	25	08/02/2006	NON-VOLATILE MEMORY AND METHOD OF MANUFACTURING FLOATING GATE	TAI, YA-HSIANG

<u>11546161</u>	Not Issued	30	10/10/2006	Source-follower type analogue buffer, compensating operation method thereof, and display therewith	TAI, YA-HSIANG
-----------------	---------------	----	------------	---	----------------

Inventor Search Completed: No Records to Display.

<b>Search Another: Inventor</b>	<b>Last Name</b>	<b>First Name</b>	<input type="button" value="Search"/>
	<input type="text" value="TAI"/>	<input type="text" value="YA-HSIANG"/>	

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)



**PALM INTRANET**

Day : Sunday  
Date: 3/4/2007  
Time: 11:50:42

## Inventor Name Search Result

Your Search was:

Last Name = CHIU

First Name = CHAUNG-MING

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">10249422</a>	6791631	150	04/08/2003	PIXEL STRUCTURE OF THIN-FILM TRANSISTOR LIQUID CRYSTAL DISPLAY	CHIU, CHAUNG-MING
<a href="#">10250005</a>	7116296	150	05/28/2003	[LAYOUT METHOD FOR IMPROVING IMAGE QUALITY]	CHIU, CHAUNG-MING
<a href="#">10438987</a>	6933993	150	05/16/2003	Method of forming a color filter layer on an array substrate and device thereof	CHIU, CHAUNG-MING
<a href="#">10444674</a>	Not Issued	161	05/23/2003	Display panel having embedded test circuit	CHIU, CHAUNG-MING
<a href="#">10652055</a>	6930372	150	08/29/2003	STORAGE CAPACITOR OF PLANAR DISPLAY AND FABRICATION METHOD OF SAME	CHIU, CHAUNG-MING
<a href="#">10692098</a>	6922095	150	10/23/2003	VOLTAGE LEVEL SHIFTER IMPLEMENTED BY ESSENTIALLY PMOS TRANSISTORS	CHIU, CHAUNG-MING
<a href="#">10694117</a>	Not Issued	61	10/27/2003	Scan driving circuit for use in planar display	CHIU, CHAUNG-MING
<a href="#">10714616</a>	Not Issued	30	11/18/2003	Method of charging liquid crystal display device	CHIU, CHAUNG-MING
<a href="#">10916361</a>	Not Issued	30	08/10/2004	Multi-resolution driver device	CHIU, CHAUNG-MING
<a href="#">11037052</a>	Not Issued	30	01/19/2005	Display circuit and display method	CHIU, CHAUNG-MING

**Inventor Search Completed: No Records to Display.**

<b>Search Another: Inventor</b>	<b>Last Name</b>	<b>First Name</b>	<input type="button" value="Search"/>
	<input type="text" value="CHIU"/>	<input type="text" value="CHAUNG-MING"/>	

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | Home page

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	451868	lcd or (liquid near crystal near display)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/04 09:52
S2	1012	(charg\$5 or pre near charg\$) with (transistor or switch or npn or pnp) with half near (cycle or period or interval or duration)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/04 09:53
S3	4	S1 same S2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/04 09:54
S4	28	S1 and S2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/04 10:01
S5	10099	(charg\$5 or prenear charg\$5) with (cell or capacitor) with (transistor or switch) with (cycle or period or duration or wave)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/04 10:03
S6	10099	(charg\$5 or pre near charg\$5) with (cell or capacitor) with (transistor or switch) with (cycle or period or duration or wave)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/04 10:03
S7	57	S1 same S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/04 10:03